

Atty. Docket No.: 10191/2261

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# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Martin KESSLER et al.

Serial No.

10/070,879

Filing Date

July 10, 2002

For

SYSTEM FOR PROTECTING A POWER

SEMICONDUCTOR OUTPUT STAGE THAT SWITCHES

AN INDUCTIVE LOAD

Examiner

Bentsu Ro

Art Unit

2837

Confirmation No.

2929

Mail Stop Issue Fee Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Mail Stop Issue Fee, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on:

Date:

Signature:

# TRANSMISSION OF REFERENCES AND INFORMATION DISCLOSURE STATEMENT PURSUANT TO 37 C.F.R. § 1.97(I)

Sir:

Applicants wish to be made of record the following references, listed on the attached Form PTO-1449.

It is respectfully requested that the references be placed in the United States Patent and Trademark Office file for the above-identified matter for interested members of the public in accordance with 37 C.F.R. § 1.97(i).

Dated: Angust 9, 2004 By:

Respectfully submitted,

Richard L. Mayer Reg. No. 22,490

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Applicant(s) Martin KESSLER et al.	
Filing Date July 10, 2002	Group 2837

#### **U. S. PATENT DOCUMENTS**

EXAMINER'S INITIALS	PATENT NUMBER	PATENT DATE	NAME	CLASS	SUBCLASS	FILING DATE
	6,262,510	July 17, 2001	Lungu			

## FOREIGN PATENT DOCUMENTS

						TRANSLATION	
EXAMINER'S INITIALS	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	YES	NO
	WO 96/09683*	March 28, 1996	РСТ				
					-		

<sup>\*</sup> English-language Abstract provided; mentioned in Specification.

### OTHER DOCUMENTS

EXAMINER'S INITIALS	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.

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EXAMINER		DATE CONSIDERED
L/Q dvill (LK		DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.